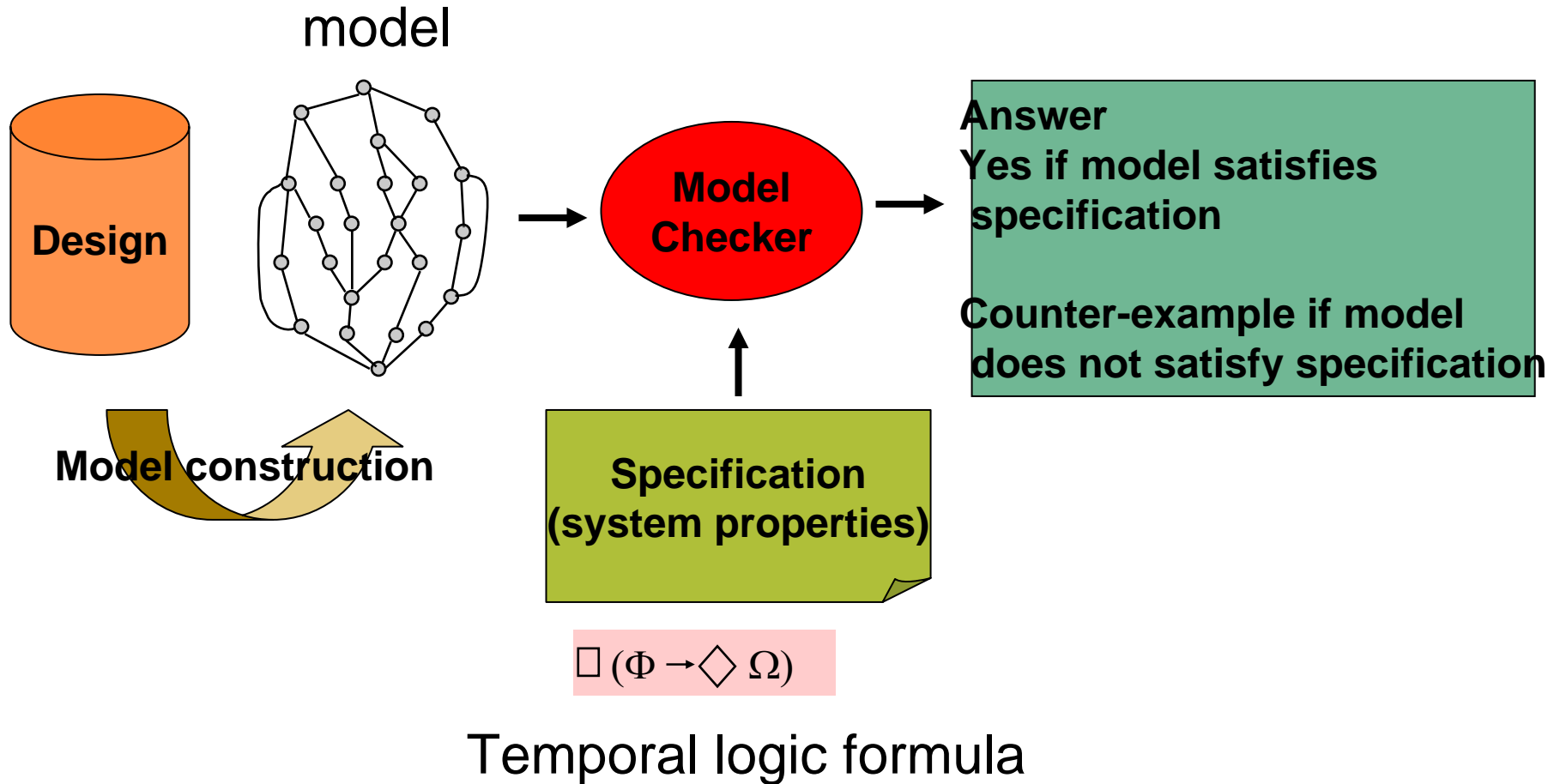


Temporal Logics and Model Checking

Outline

- Temporal Logic
 - Linear
 - LPTL (Linear time Propositional Temporal Logics)
 - Branching
 - CTL (Computation Tree Logics)
 - CTL* (the full branching temporal logics)
 - Models
 - Kripke structure
 - Timed automata (TA)
 - Communicating Timed Automata (CTA)
 - Model checking example
 - A SPIN example for mutual exclusion problem
 - A RED example for fisher's timed mutual exclusion algorithm
-

Model Checking Framework



Temporal Logics

□ Linear

- LPTL (Linear time Propositional Temporal Logics)

□ Branching

- CTL (Computation Tree Logics)
 - CTL* (the full branching temporal logics)
-

Temporal Logics : Catalog

propositional	\leftrightarrow	first-order
global	\leftrightarrow	compositional
branching	\leftrightarrow	linear-time
points	\leftrightarrow	intervals
discrete	\leftrightarrow	continuous
past	\leftrightarrow	future

Linear Time Propositional Temporal Logics (LPTL)

Basic assumption :

- Isomorphism: $(N, <)$
 - discrete ; suitable for digital computer
 - Initial point (0) ; computer needs reboot
 - Infinite future ; finite and infinite
- Every element in N is a state
 - Every state only have one successor

LPTL

Conventional notation :

- propositions : $\mathbf{p, q, r, \dots}$
- sets : $\mathbf{A, B, C, D, \dots}$
- states : \mathbf{s}
- state sequences : \mathbf{S}
- formulas : φ, ϕ
- Set of natural number : $N = \{0, 1, 2, 3, \dots\}$
- Set of real number : R

LPTL

Given P : a set of propositions,
a Linear-time structure : *state sequence*

$$S = s_0 s_1 s_2 s_3 s_4 \dots s_k \dots$$

s_k is a function of P where $P \rightarrow \{true, false\}$

Syntax of LPTL

$$\phi ::= \mathbf{true} \mid \mathbf{p} \mid \neg \phi \mid \phi_1 \vee \phi_2 \mid \bigcirc \phi \mid \phi_1 U \phi_2$$

abbreviation

$$\mathbf{false} \equiv \neg \mathbf{true}$$

$$\phi_1 \wedge \phi_2 \equiv \neg ((\neg \phi_1) \vee (\neg \phi_2))$$

$$\phi_1 \rightarrow \phi_2 \equiv (\neg \phi_1) \vee \phi_2$$

$$\Diamond \phi \equiv \mathbf{true} U \phi$$

$$\Box \phi \equiv \neg \Diamond \neg \phi$$

Syntax of LPTL

Exam.	Symbol in CMU
-------	------------------

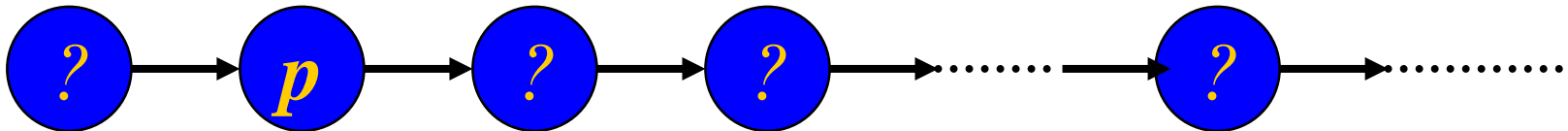
$\bigcirc p$	Xp	<i>p</i> is true on next state
$p \cup q$	$p \cup q$	From now on, <i>p</i> is always true until <i>q</i> is true
$\Diamond p$	Fp	From now on, there will be a state where <i>p</i> is eventually (sometimes) true
$\Box p$	Gp	From now on, <i>p</i> is always true

Syntax of LPTL

$\bigcirc p$

$\mathbf{X}p$

p is true on **next** state



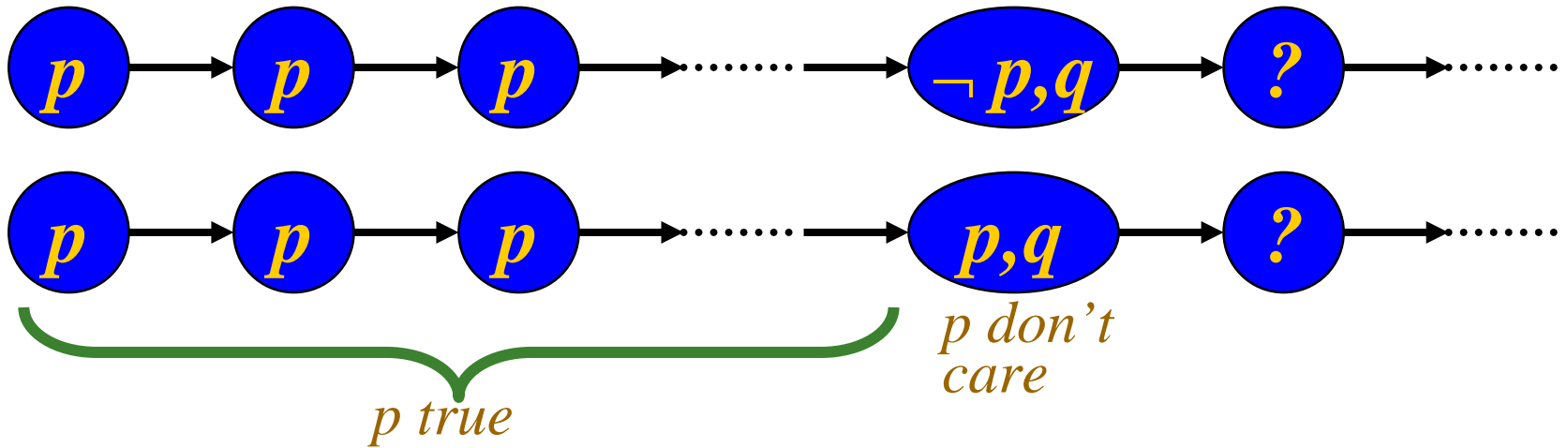
? : don't care

Syntax of LPTL

$p \cup q$

$p \cup q$

From now on, p is always true **until** q is true

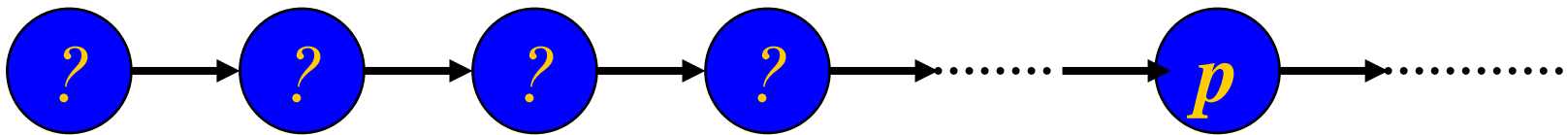


Syntax of LPTL

$\Diamond p$

Fp

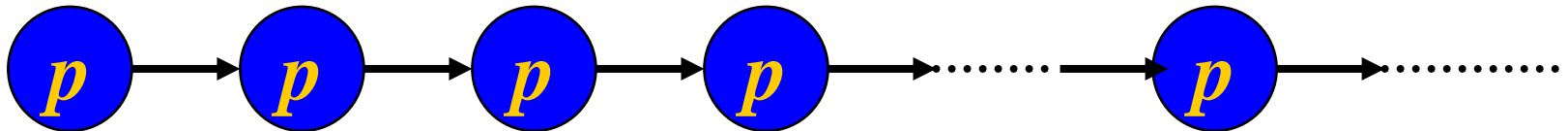
From now on, there will be a state where p is **eventually (sometimes)** true



$\Box p$

Gp

From now on, p is **always** true



Syntax of LPTL

Two operator for Fairness

- $\Diamond^\infty p \equiv \Box \Diamond p$; **p will happen infinitely many times**
infinitely often
- $\Box^\infty p \equiv \Diamond \Box p$; **p will be always true after some time in the future**
almost everywhere

Semantics of LPTL

suffix path :

$$S = s_0 s_1 s_2 s_3 s_4 s_5 \dots\dots\dots$$

$$S^{(0)} = s_0 s_1 s_2 s_3 s_4 s_5 \dots\dots\dots$$

$$S^{(1)} = s_1 s_2 s_3 s_4 s_5 s_6 \dots\dots\dots$$

$$S^{(2)} = s_2 s_3 s_4 s_5 s_6 \dots\dots\dots$$

$$S^{(3)} = s_3 s_4 s_5 s_6 \dots\dots\dots$$

$$S^{(k)} = s_k s_{k+1} s_{k+2} s_{k+3} \dots\dots\dots$$

Semantics of LPTL

Suppose there is a state sequence

$$\mathbf{S} = s_0 s_1 s_2 s_3 s_4 \dots s_k \dots$$

We define $\mathbf{S} \models \phi$ (\mathbf{S} **satisfies** ϕ) as :

- $\mathbf{S} \models \text{true}$
- $\mathbf{S} \models p \Leftrightarrow s_0(p) = \text{true}, \text{ or equivalently } p \in s_0$
- $\mathbf{S} \models \neg \phi \Leftrightarrow \mathbf{S} \models \phi \text{ is false}$
- $\mathbf{S} \models \phi_1 \vee \phi_2 \Leftrightarrow \mathbf{S} \models \phi_1 \text{ or } \mathbf{S} \models \phi_2$
- $\mathbf{S} \models \bigcirc \phi \Leftrightarrow \mathbf{S}^{(1)} \models \phi$
- $\mathbf{S} \models \phi_1 U \phi_2 \Leftrightarrow \exists k \geq 0 (\mathbf{S}^{(k)} \models \phi_2 \wedge \forall 0 \leq j < k (\mathbf{S}^{(j)} \models \phi_1))$

Semantics of LPTL

- Assume there is a state sequence **S** which satisfies φ
(**S** $\models \varphi$)
then **S** is one of the model of φ .
- Assume there is a state sequence **S** **sat** φ ,
then φ is **satisfiable**; otherwise φ is **unsatisfiable** ◦
- If for all state sequence **S** $\not\models \varphi$,
then φ is **valid** ◦ ($\models \varphi$)

Semantics of LPTL

example : $\Box (\textit{found enemy} \rightarrow \Diamond \textit{destroy enemy})$

- Can't conveniently express enemies appear concurrently.

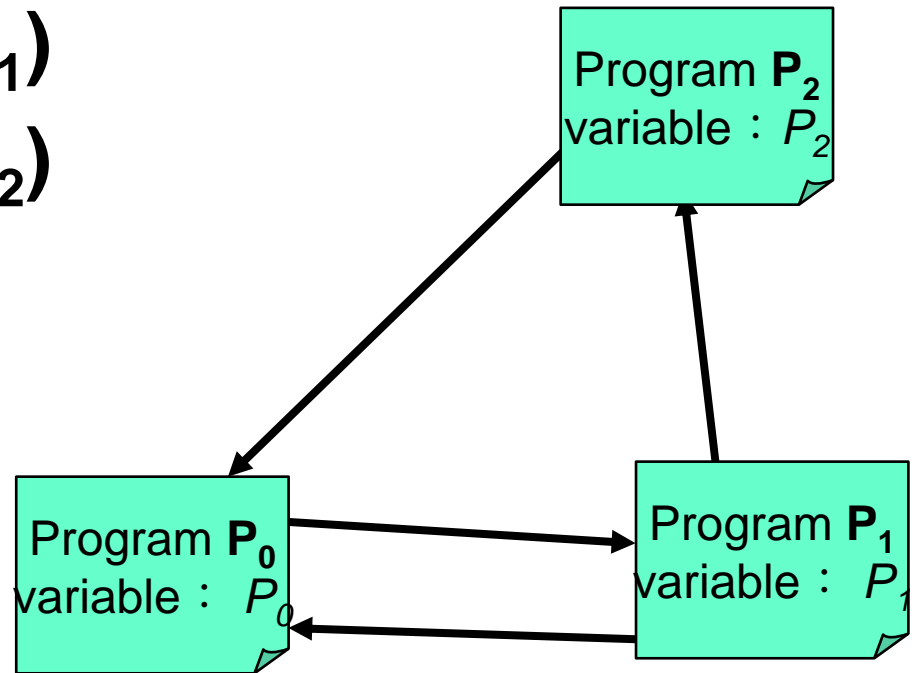
example : $\Box \neg (\textit{\textbf{A}} \textit{ is executing} \wedge \textit{\textbf{B}} \textit{ is executing})$

Example of writing LPTL formula (I)

$P_0: (p_0 := 0 \mid p_0 := p_0 \vee p_1 \vee p_2)$

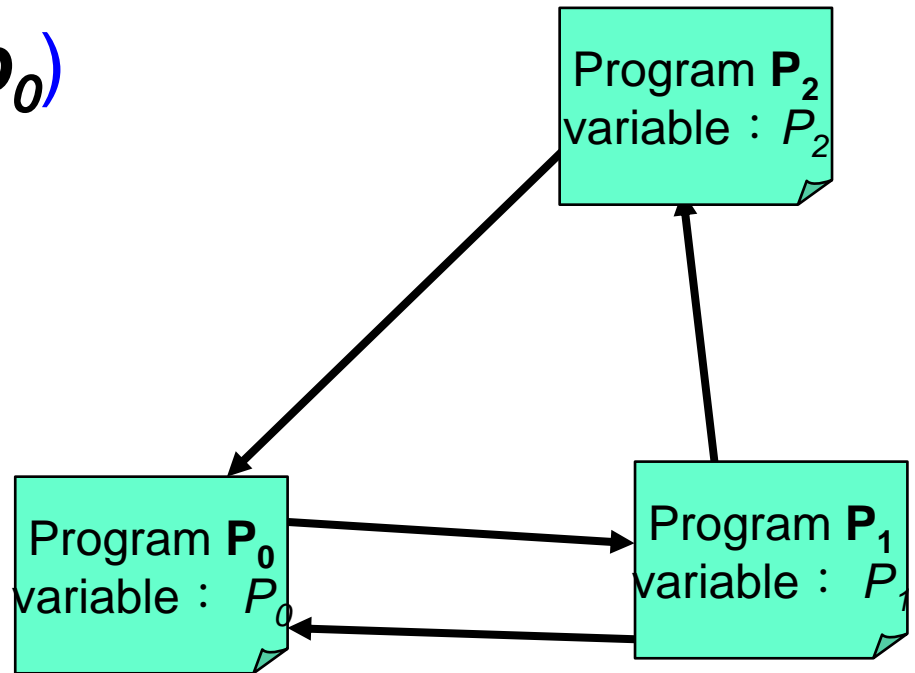
$P_1: (p_1 := 0 \mid p_1 := p_0 \vee p_1)$

$P_2: (p_2 := 0 \mid p_2 := p_1 \vee p_2)$



Example of writing LPTL formula (I)

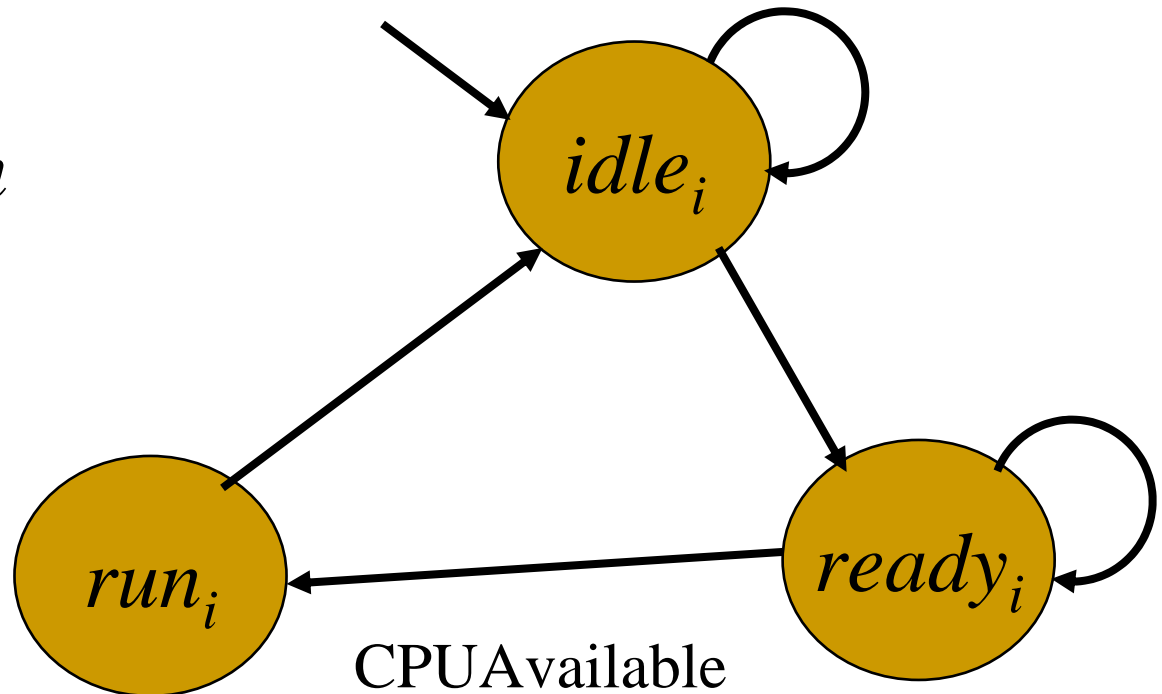
$\square($ $\bigcirc \neg p_0$
 $\vee ((p_0 \vee p_1 \vee p_2) \leftrightarrow \bigcirc p_0)$
 $\vee \bigcirc \neg p_1$
 $\vee ((p_0 \vee p_1) \leftrightarrow \bigcirc p_1)$
 $\vee \bigcirc \neg p_2$
 $\vee ((p_1 \vee p_2) \leftrightarrow \bigcirc p_2)$
 $)$



Example of writing LPTL formula (II)

Process_{*i*}, $1 \leq i \leq m$

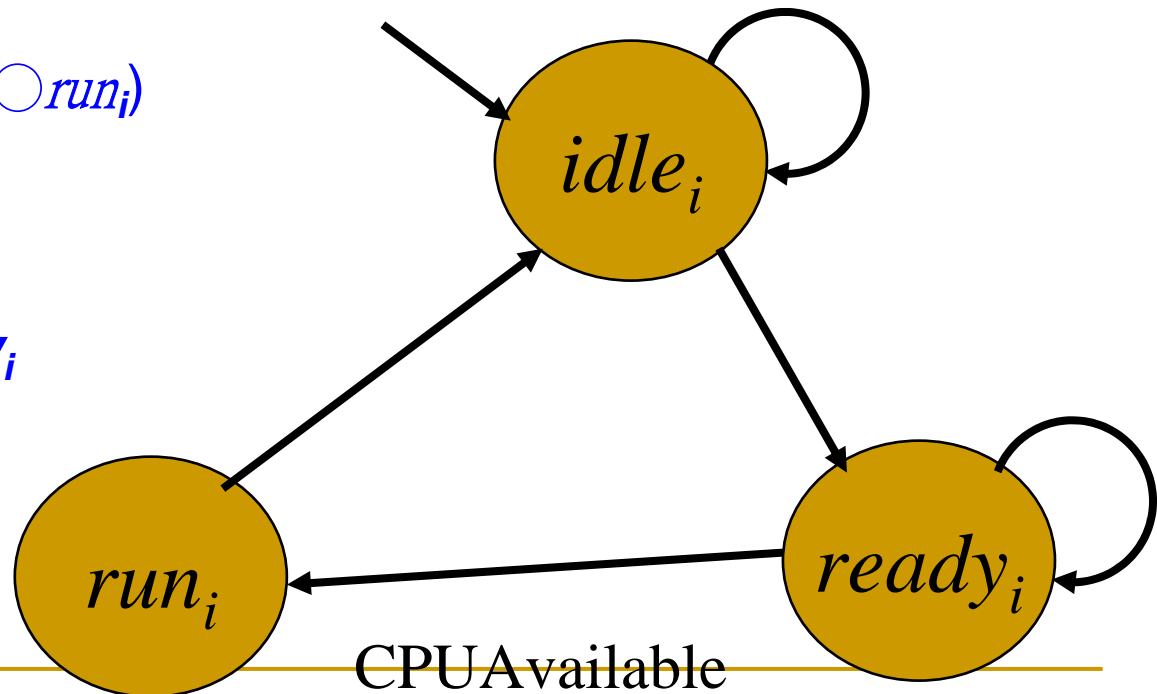
Also describe the
mutual exclusion
condition



Example of writing LPTL formula (II)

$\bigwedge_{1 \leq i \leq m} \text{idle}_i$

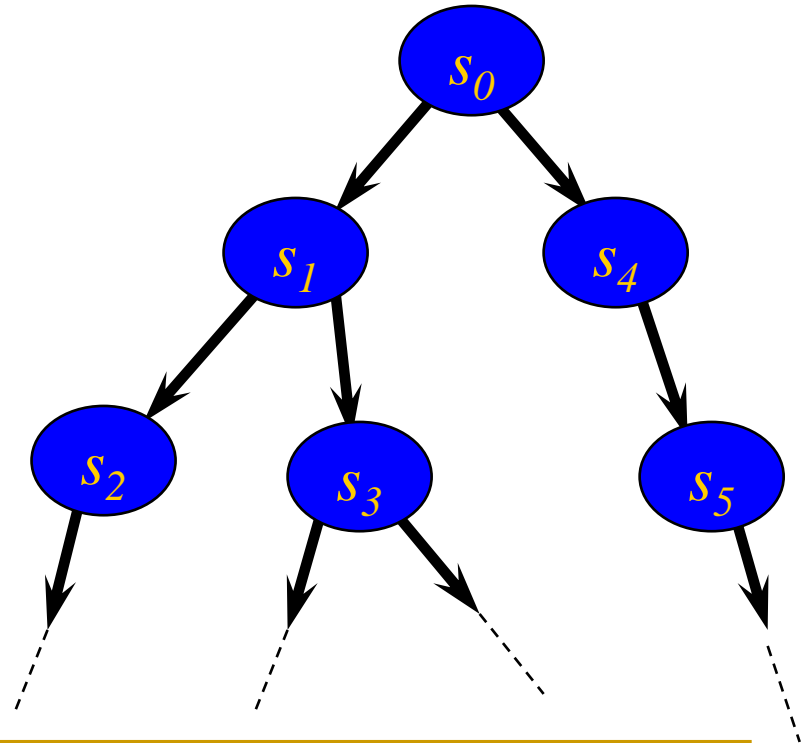
$\bigwedge_{1 \leq i \leq m} \square ($
 $(\text{idle}_i \leftrightarrow (\bigcirc \text{idle}_i \vee \bigcirc \text{ready}_i))$
 $\vee (\text{ready}_i \leftrightarrow \bigcirc \text{ready}_i)$
 $\vee ((\text{idle}_i \wedge \bigwedge_{1 \leq j \leq m} \neg \text{run}_j) \leftrightarrow \bigcirc \text{run}_i)$
 $\vee (\text{run}_i \leftrightarrow \bigcirc \text{idle}_i)$
 $\vee ($
 $\text{run}_i \leftrightarrow \bigcirc \text{run}_i$
 $\wedge \text{idle}_i \leftrightarrow \bigcirc \text{idle}_i$
 $\wedge \text{ready}_i \leftrightarrow \bigcirc \text{ready}_i$
 $)$
 $)$



Branching Temporal Logic

Basic assumption of tree-like structure

- Every **node** is a function of $P \rightarrow \{\text{true}, \text{false}\}$
- Every state may have many **successors**



Branching Temporal Logic

Basic assumption of tree-like structure

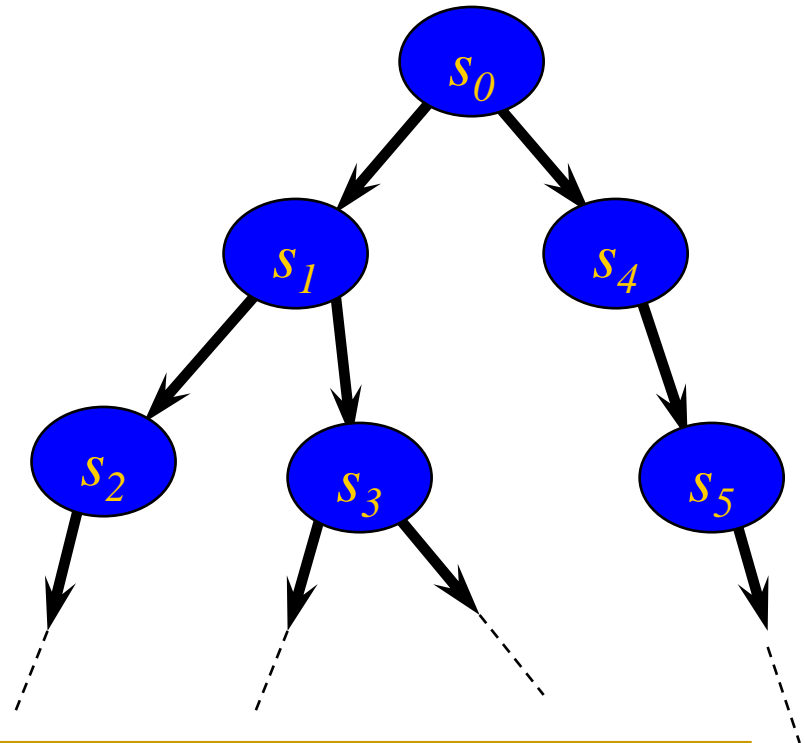
- Every **path** is isomorphic as N
 - Correspond to a **state sequence**

Path : $s_0 \ s_1 \ s_3 \ \dots\dots$

$s_0 \ s_1 \ s_2 \ \dots\dots$

$s_1 \ s_3 \ \dots\dots$

$s_4 \ s_5 \ \dots\dots$



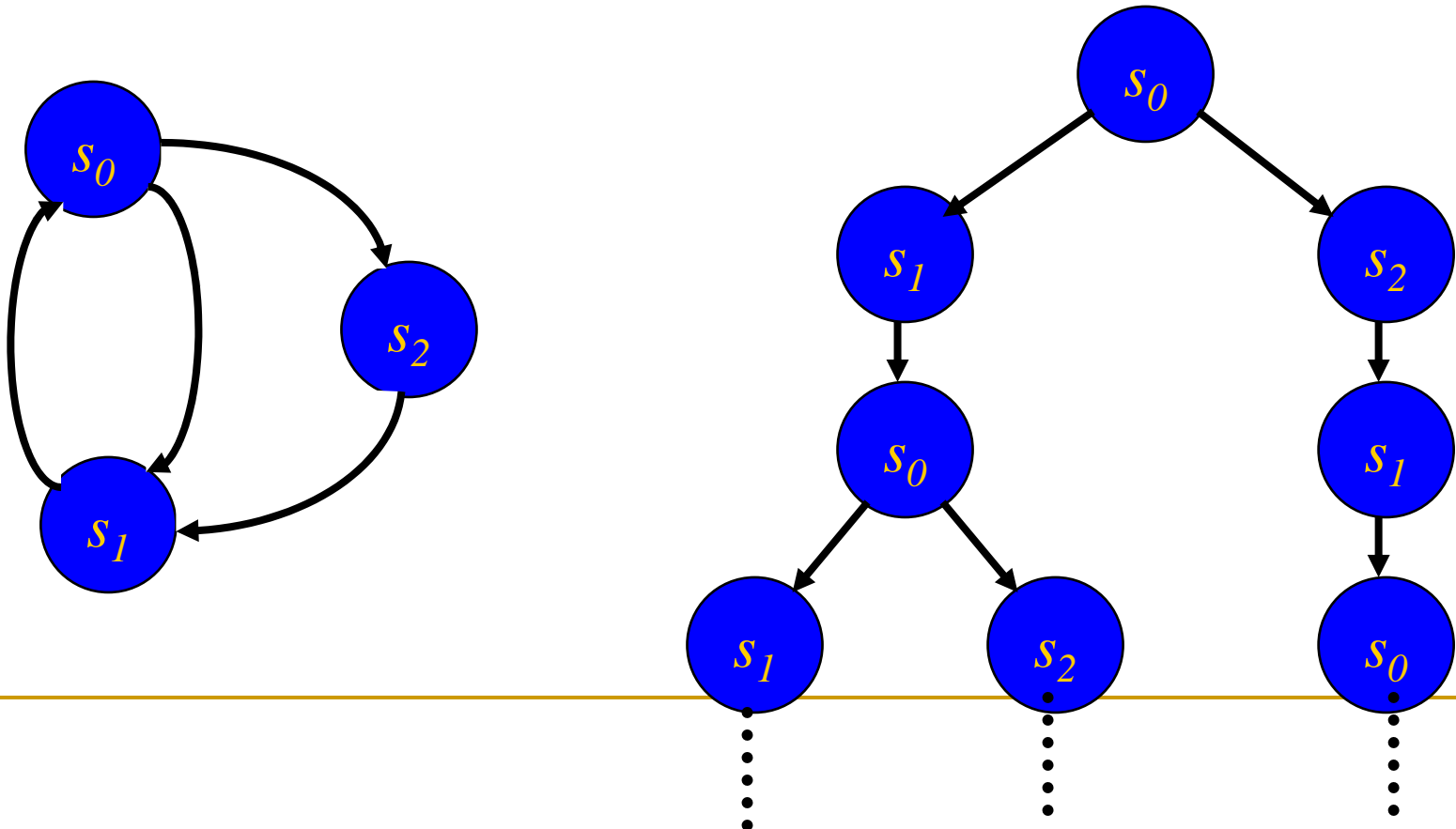
Branching Temporal Logic

It can accommodate infinite and dense state successors

- In CTL and CTL*, it can't tell
 - Finite and infinite
 - Is there infinite transitions ?
 - Dense and discrete
 - Is there countable (ω) transitions ?

Branching Temporal Logic

Get by flattening a finite state machine



Syntax of CTL(Computation Tree Logic)

$$\varphi ::= \mathbf{true} \mid \mathbf{p} \mid \neg \varphi \mid \varphi_1 \vee \varphi_2 \mid \exists \bigcirc \varphi \mid \forall \bigcirc \varphi \\ \mid \exists \varphi_1 \cup \varphi_2 \mid \forall \varphi_1 \cup \varphi_2$$

abbreviation :

\mathbf{false}	\equiv	$\neg \mathbf{true}$
$\varphi_1 \wedge \varphi_2$	\equiv	$\neg ((\neg \varphi_1) \vee (\neg \varphi_2))$
$\varphi_1 \rightarrow \varphi_2$	\equiv	$(\neg \varphi_1) \vee \varphi_2$
$\exists \Diamond \varphi$	\equiv	$\exists \mathbf{true} \cup \varphi$
$\forall \Box \varphi$	\equiv	$\neg \exists \Diamond \neg \varphi$
$\forall \Diamond \varphi$	\equiv	$\forall \mathbf{true} \cup \varphi$
$\exists \Box \varphi$	\equiv	$\neg \forall \Diamond \neg \varphi$

Semantics of CTL

example	symbol in CMU
---------	------------------

$\exists \bigcirc p$	EXp	there exists a path where p is true on next state
----------------------	-------	---

$\exists p \cup q$	$pEUq$	from now on, there is a path where p is always true until q is true
--------------------	--------	---

$\forall \bigcirc p$	AXp	for all path where p is true on next state
----------------------	-------	--

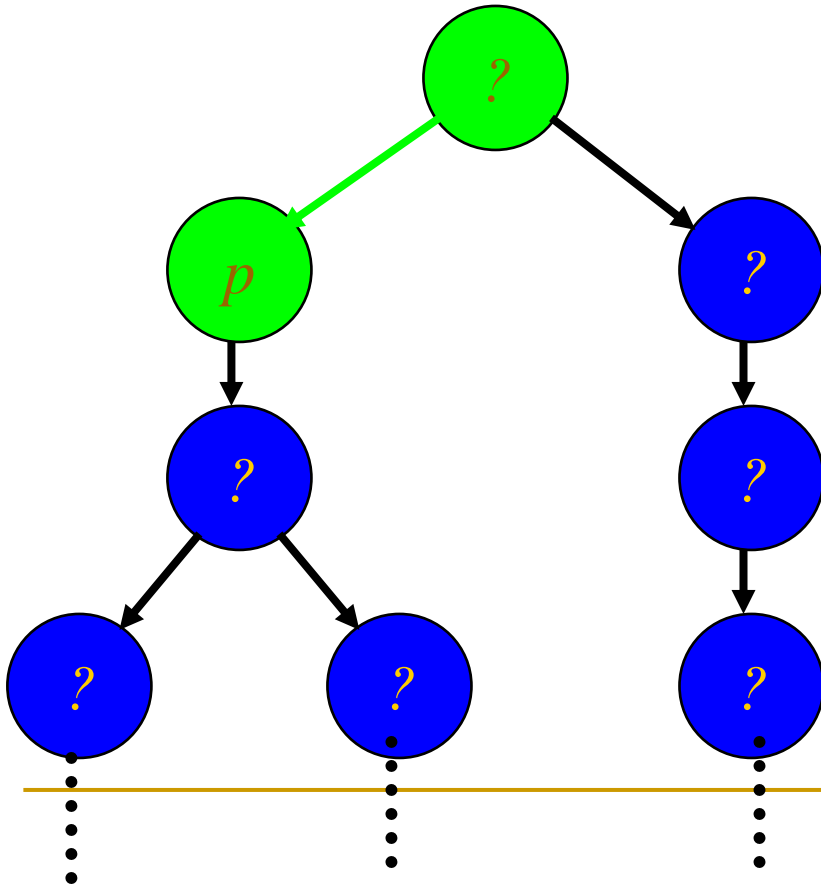
$\forall p \cup q$	$pAUq$	from now on, for all path where p is always true until q is true
--------------------	--------	--

Semantics of CTL

$\exists \bigcirc p$

EXp

there exists a path where p is true on next state

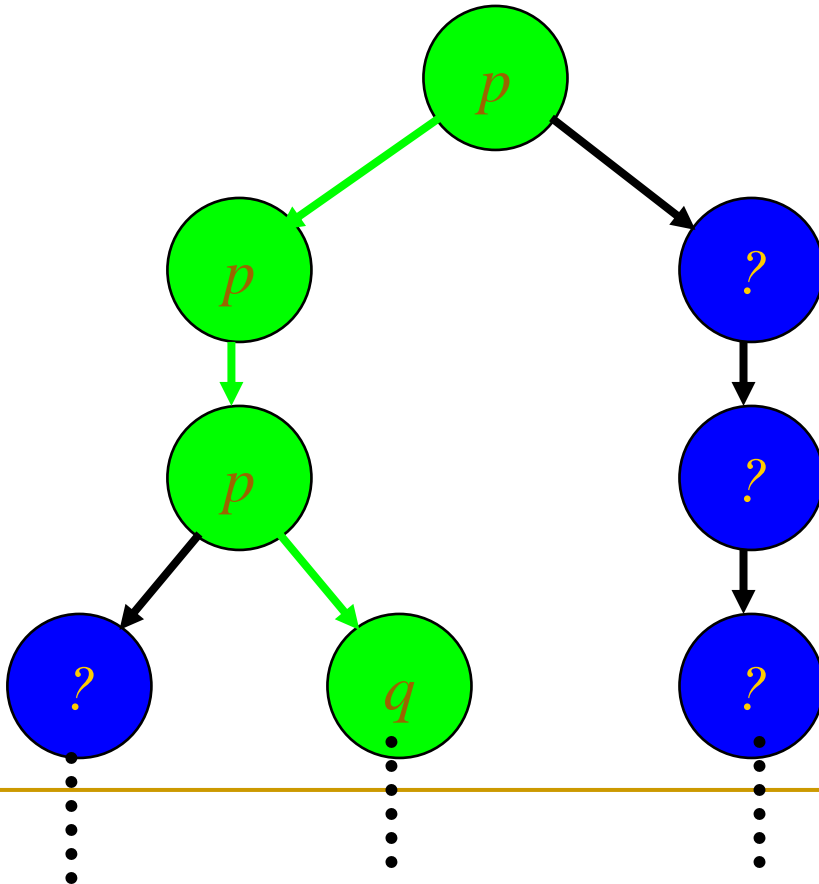


Semantics of CTL

$\exists p \cup q$

$p \text{EU} q$

from now on, there is a path where p is always true until q is true

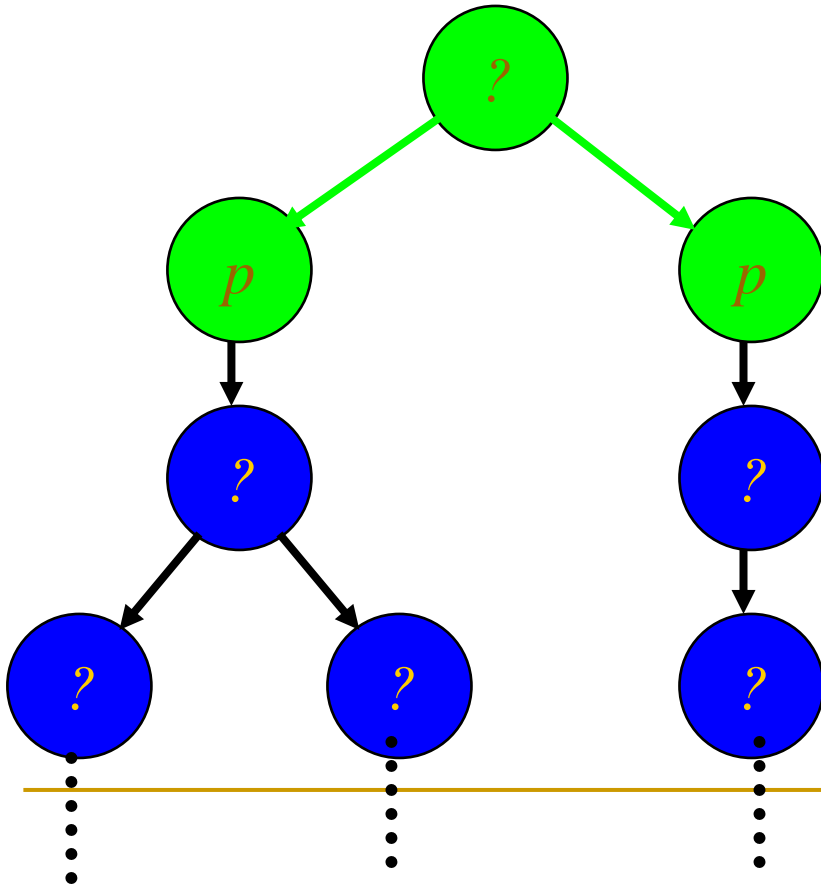


Semantics of CTL

 $\forall \textcircled{O} p$

AXp

for all path where p is true on next state

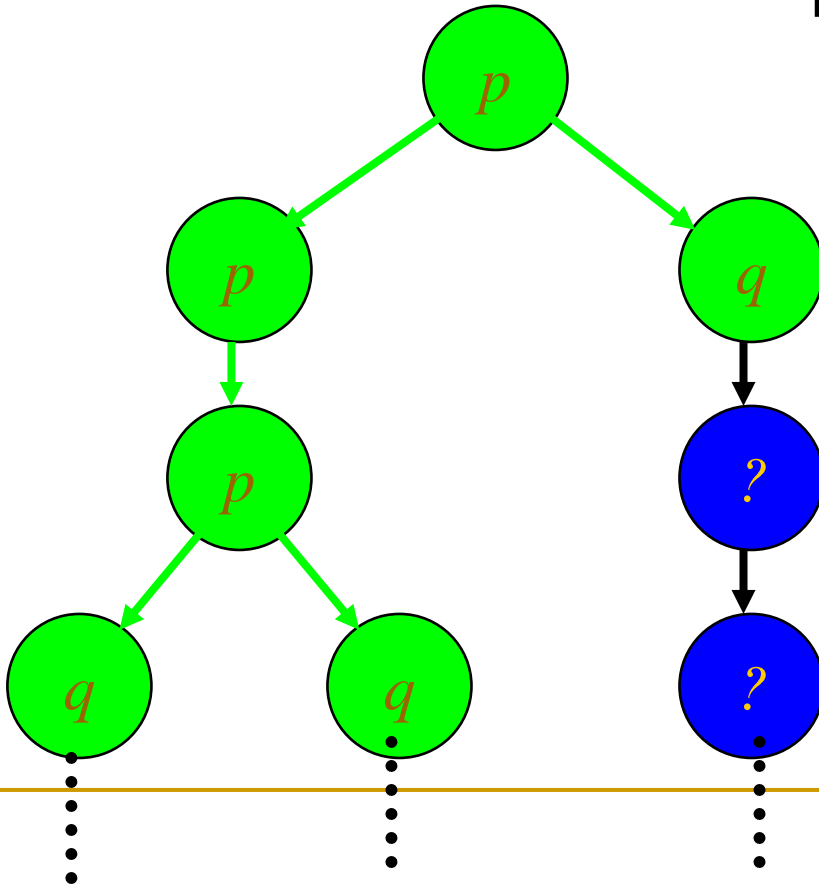


Semantics of CTL

$\forall p \cup q$

$p \text{AU} q$

from now on, for all path
where p is always true until q
is true



Semantic of CTL

Assume there is a tree structure M 、one state s in M and a CTL fomula φ

Define $M, s \models \varphi$ which means s in M satisfy φ

Semantics of CTL

s-path : a path in M
which starts from s

s_0 -path:

$s_0 s_1 s_2 s_3 s_5 \dots$

$s_0 s_1 s_6 s_7 s_8 \dots$

s_1 -path:

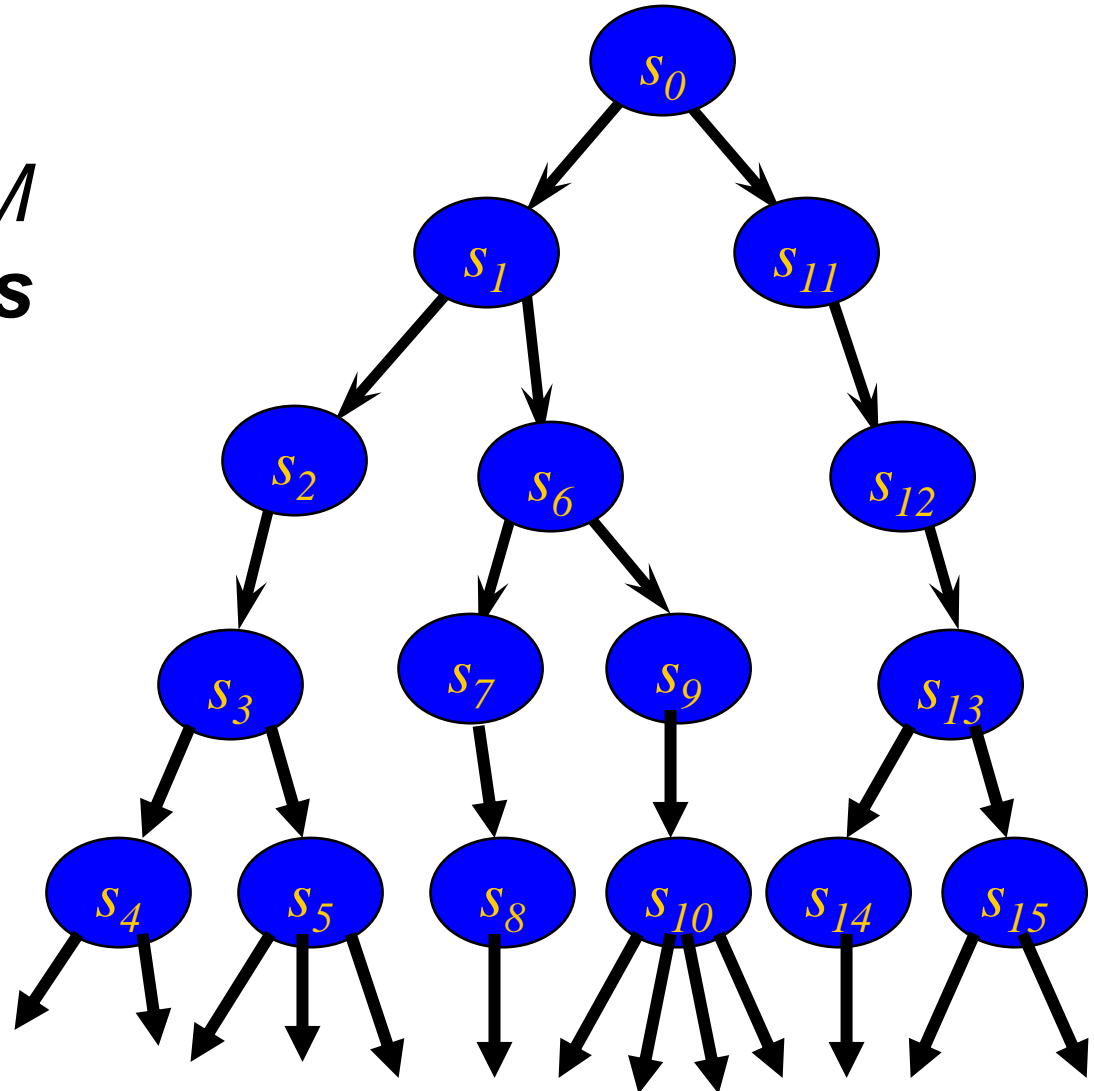
$s_1 s_2 s_3 s_5 \dots$

s_2 -path:

$s_2 s_3 s_5 \dots$

s_{13} -path:

$s_{13} s_{15} \dots$



Semantics of CTL

- $M, s \models \text{true}$
- $M, s \models p \Leftrightarrow p \in s$
- $M, s \models \neg \varphi \Leftrightarrow M, s \models \varphi$ 是 *false*
- $M, s \models \varphi_1 \vee \varphi_2 \Leftrightarrow M, s \models \varphi_1$ or $M, s \models \varphi_2$
- $M, s \models \exists O \varphi \Leftrightarrow \exists \text{ s-path} = s_0 s_1 \dots (M, s_1 \models \varphi)$
- $M, s \models \forall O \varphi \Leftrightarrow \forall \text{ s-path} = s_0 s_1 \dots (M, s_1 \models \varphi)$
- $M, s \models \exists \varphi_1 \cup \varphi_2 \Leftrightarrow \exists \text{ s-path} = s_0 s_1 \dots, \exists k \geq 0$
 $(M, s_k \models \varphi_2 \wedge \forall 0 \leq j < k (M, s_j \models \varphi_1))$
- $M, s \models \forall \varphi_1 \cup \varphi_2 \Leftrightarrow \forall \text{ s-path} = s_0 s_1 \dots, \exists k \geq 0$
 $(M, s_k \models \varphi_2 \wedge \forall 0 \leq j < k (M, s_j \models \varphi_1))$

Syntax of CTL*

- **CTL*** formula (**state**-formula)

$$\varphi ::= true \mid p \mid \neg \varphi_1 \mid \varphi_1 \vee \varphi_2 \mid \exists \phi \mid \forall \phi$$

- **path**-formula

$$\phi ::= \varphi \mid \neg \phi_1 \mid \phi_1 \vee \phi_2 \mid \bigcirc \phi_1 \mid \phi_1 U \phi_2$$

CTL* is set of all **state**-formula !

Example of CTL*

- In a fair concurrent environment, jobs will eventually finish.

$$\forall(((\Box\Diamond\textit{execute}_1) \wedge (\Box\Diamond\textit{execute}_2)) \rightarrow \Diamond\textit{finish})$$

or

$$\forall(((\Diamond^\infty\textit{execute}_1) \wedge (\Diamond^\infty\textit{execute}_2)) \rightarrow \Diamond\textit{finish})$$

Semantics of CTL*

suffix path :

$S = s_0 s_1 s_2 s_3 s_5 \dots$

$S^{(0)} = s_0 s_1 s_2 s_3 s_5 \dots$

$S^{(1)} = s_1 s_2 s_3 s_5 \dots$

$S^{(2)} = s_2 s_3 s_5 \dots$

$S^{(3)} = s_3 s_5 \dots$

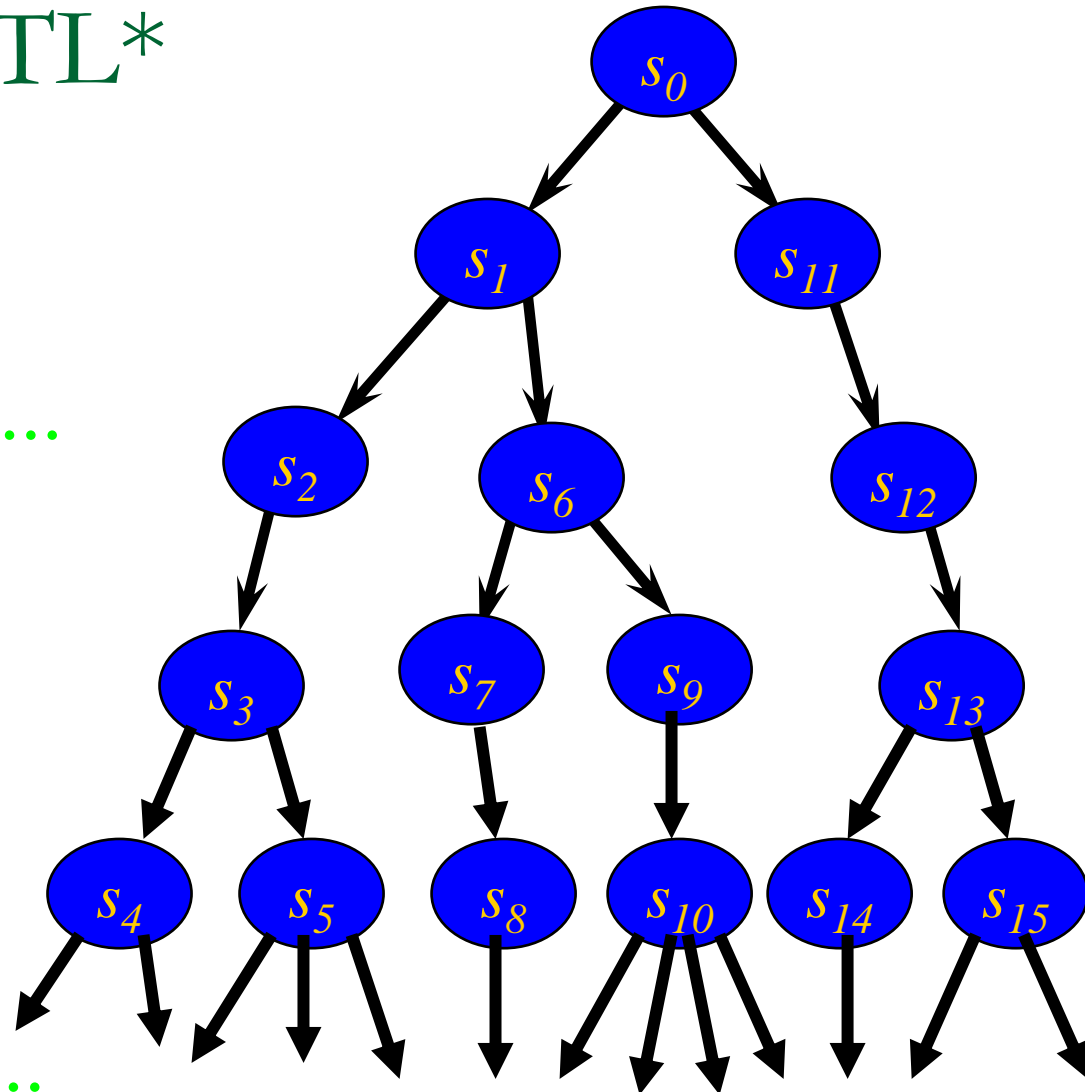
$S^{(4)} = s_5 \dots$

$S = s_0 s_1 s_6 s_7 s_8 \dots$

$S^{(2)} = s_6 s_7 s_8 \dots$

$S = s_0 s_{11} s_{12} s_{13} s_{15} \dots$

$S^{(3)} = s_{13} s_{15} \dots$



Semantics of CTL*

state-formula

$$\varphi ::= \text{true} \mid p \mid \neg \varphi_1 \mid \varphi_1 \vee \varphi_2 \mid \exists \phi \mid \forall \phi$$

- $M, s \models \text{true}$
- $M, s \models p \Leftrightarrow p \in s$
- $M, s \models \neg \varphi \Leftrightarrow M, s \models \varphi$ 是 **false**
- $M, s \models \varphi_1 \vee \varphi_2 \Leftrightarrow M, s \models \varphi_1$ or $M, s \models \varphi_2$
- $M, s \models \exists \phi \Leftrightarrow \exists \text{ s-path} = S (S \models \phi)$
- $M, s \models \forall \phi \Leftrightarrow \forall \text{ s-path} = S (S \models \phi)$

Semantics of CTL*

path-formula

$$\phi ::= \varphi \mid \neg \phi_1 \mid \phi_1 \vee \phi_2 \mid \bigcirc \phi \mid \phi_1 U \phi_2$$

- If $S = s_0 s_1 s_2 s_3 s_4 \dots$, $S \models \varphi \Leftrightarrow M, s_0 \models \varphi$
- $S \models \neg \phi_1 \Leftrightarrow S \models \phi_1$ 是 **false**
- $S \models \phi_1 \vee \phi_2 \Leftrightarrow S \models \phi_1$ or $S \models \phi_2$
- $S \models \bigcirc \phi \Leftrightarrow S^{(1)} \models \phi$
- $S \models \phi_1 U \phi_2 \Leftrightarrow \exists k \geq 0 (S^{(k)} \models \phi_2 \wedge \forall 0 \leq j < k (S^{(j)} \models \phi_1))$

Models

- Kripke Structure
- Timed Automata (TA)
- Communicating Timed Automata (CTA)

Kripke Structure

- A Kripke structure M over a set of atomic propositions, AP , is a four tuple $M = (S, S_0, R, L)$ where
 - S is a finite set of states.
 - $S_0 \subseteq S$ is the set of initial states.
 - $R \subseteq S \times S$ is a transition relation that must be total, that is $\forall s \in S. \exists s'. R(s, s')$
 - $L: S \rightarrow 2^{AP}$ is a function that labels each state with the set of atomic propositions true in that state.

Example of Kripke Structure

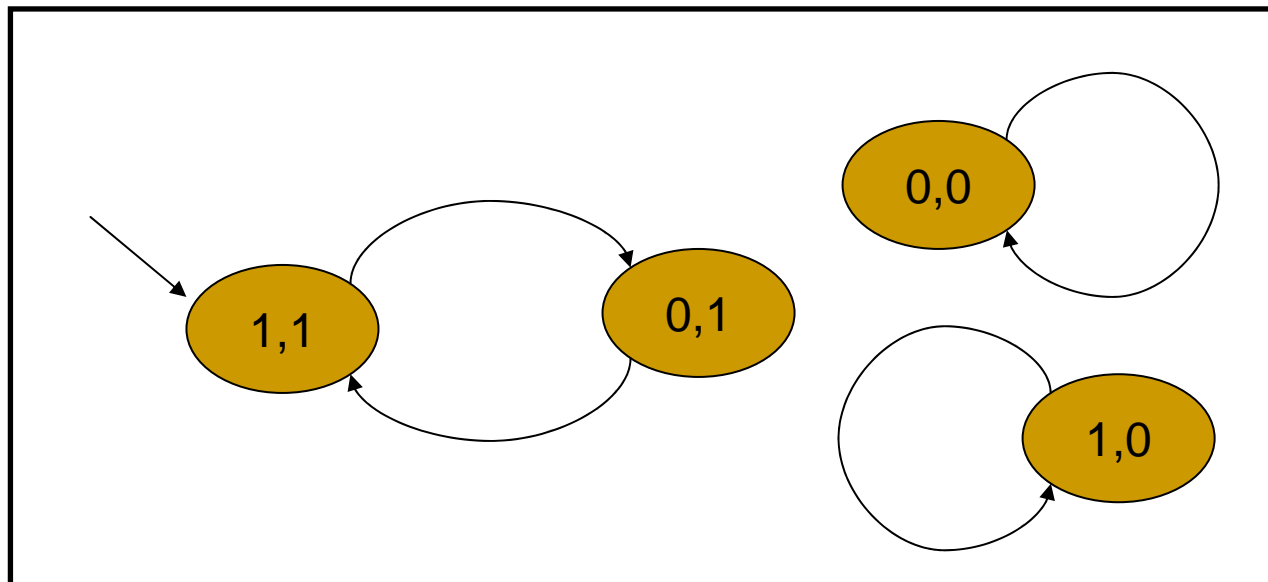
Suppose there is a program

```
initially x=1 and y=1;  
while true do  
  x:=(x+y) mod 2;  
endwhile
```

where x and y range over $D=\{0,1\}$

Example of Kripke Structure

- $S = D \times D$
- $S_0 = \{(1,1)\}$
- $R = \{((1,1), (0,1)), ((0,1), (1,1)), ((1,0), (1,0)), ((0,0), (0,0))\}$
- $L((1,1)) = \{x=1, y=1\}, L((0,1)) = \{x=0, y=1\}, L((1,0)) = \{x=1, y=0\}, L((0,0)) = \{x=0, y=0\}$



Timed Automata

$$\mathbf{A} = \langle \mathbf{Q}, q_0, \mathbf{P}, \mathbf{X}, \mu, \mathbf{E}, \tau, \pi \rangle$$

\mathbf{Q} : set of control locations

q_0 : initial location

\mathbf{P} : set of propositions

\mathbf{X} : set of clock variables

$\mu : \mathbf{Q} \rightarrow \Gamma(\mathbf{P}, \mathbf{X})$; invariant

$\mathbf{E} \subseteq \mathbf{Q} \times \mathbf{Q}$: set of transitions

$\tau : \mathbf{E} \rightarrow \Gamma(\mathbf{P}, \mathbf{X})$; triggering condition

$\pi : \mathbf{E} \rightarrow 2^{\mathbf{X}}$: set of clocks to be reset

Example of Timed Automata

- Suppose we will launch a missile which will aim at enemy and fix its direction every 50ms until the missile hits the target in 500ms.

Example of Timed Automata

$$A = \langle Q, q_0, P, X, \mu, E, \tau, \pi \rangle$$

$$Q = \{\text{aim}, \text{hit}\}$$

$$E = \{(\text{aim}, \text{aim}), (\text{aim}, \text{hit})\}$$

$$q_0 = \text{aim}$$

$$\tau(\text{aim}, \text{aim}) = z = 50$$

$$P = \{\}, \quad X = \{x, y\}$$

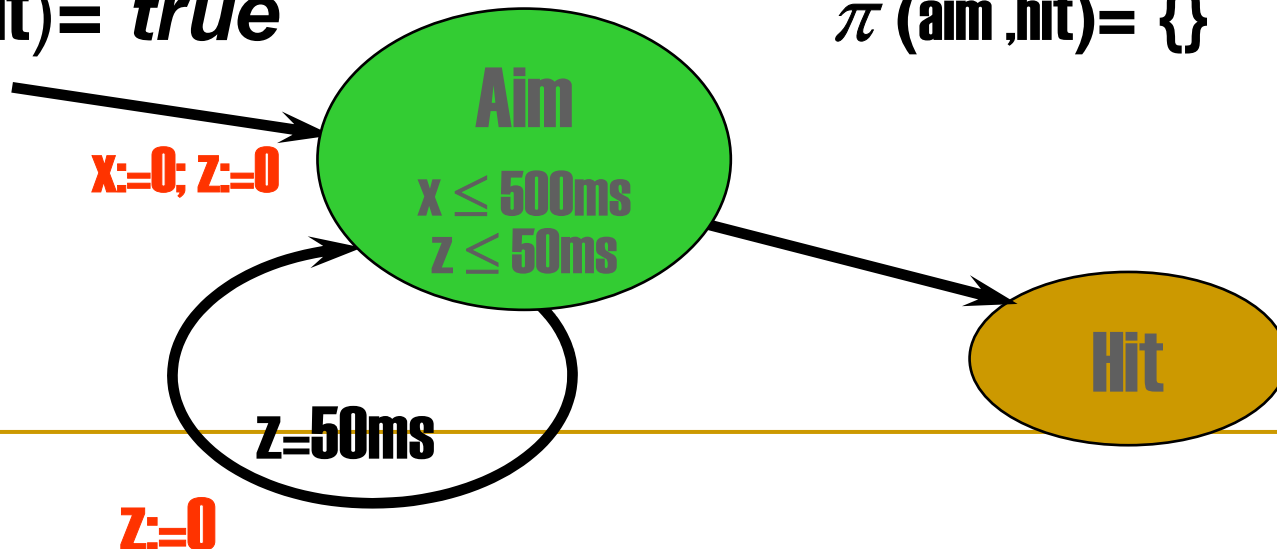
$$\tau(\text{aim}, \text{hit}) = \text{true}$$

$$\mu(\text{aim}) = x \leq 500 \wedge z \leq 50$$

$$\pi(\text{aim}, \text{aim}) = \{z\}$$

$$\mu(\text{hit}) = \text{true}$$

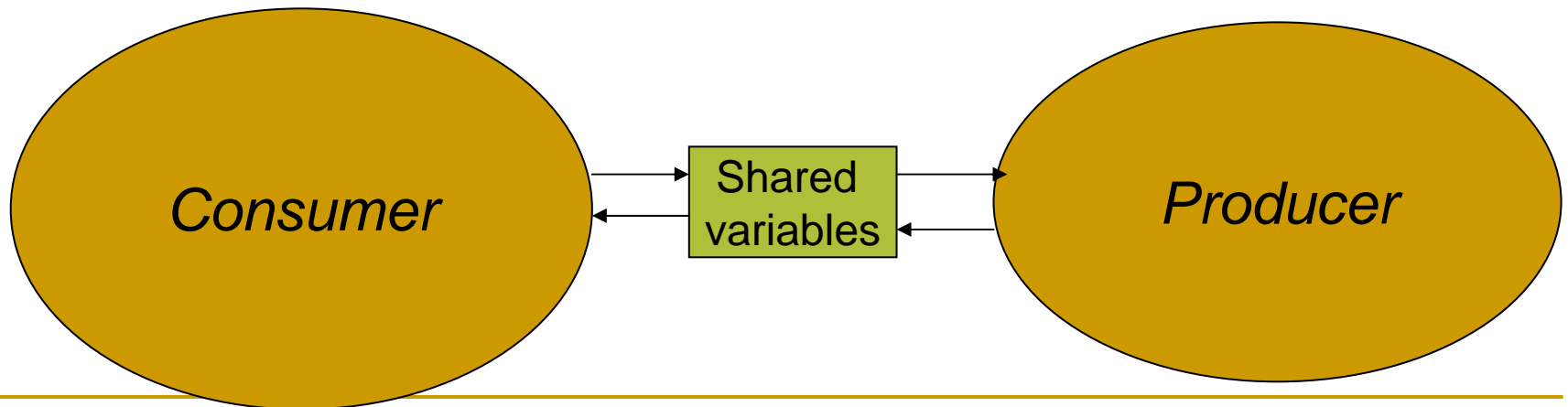
$$\pi(\text{aim}, \text{hit}) = \{\}$$



Communicating TA (CTA)

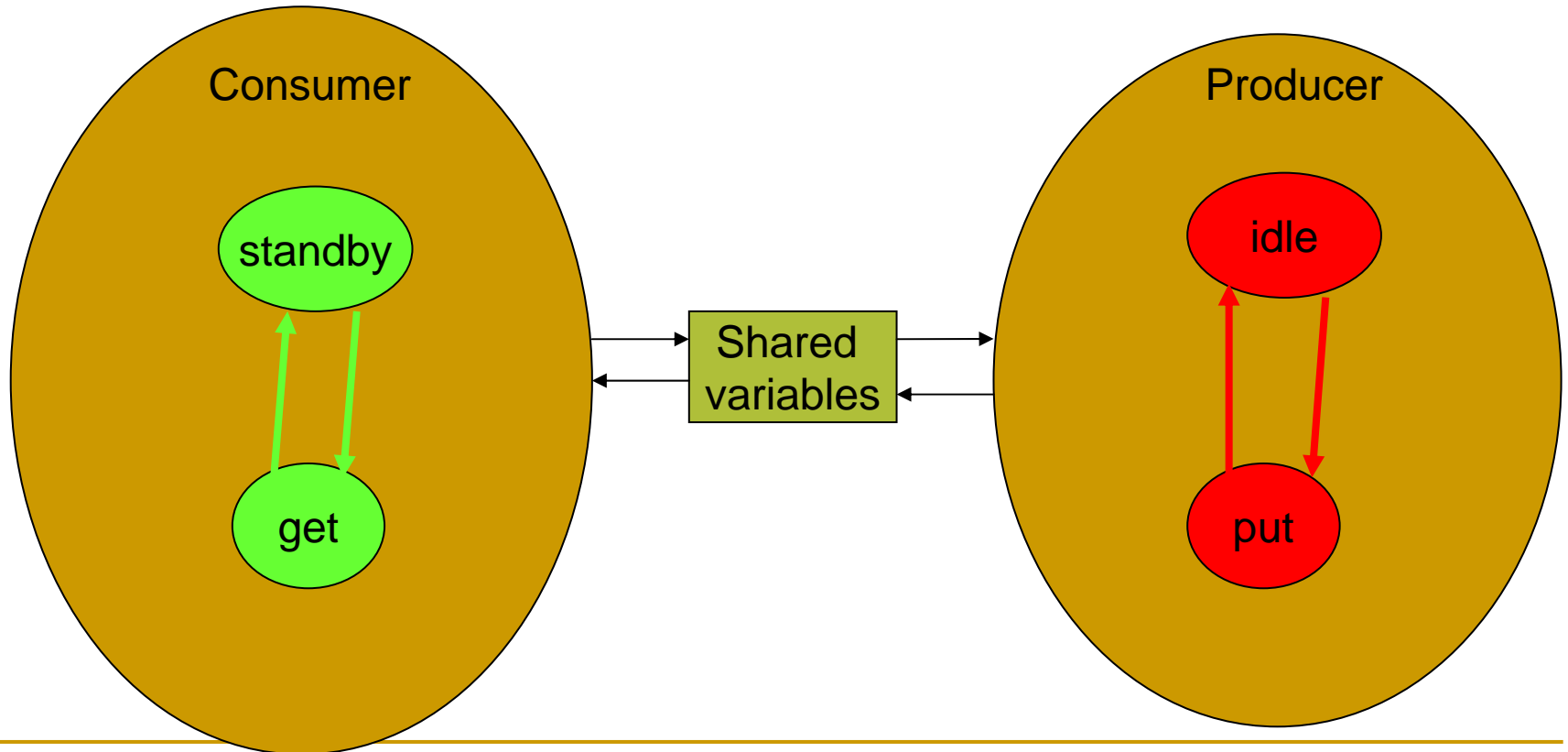
A set of TAs, communicate each other by synchronizers and shared variables

What is a legal concurrent computing ?



Communicating TA (CTA)

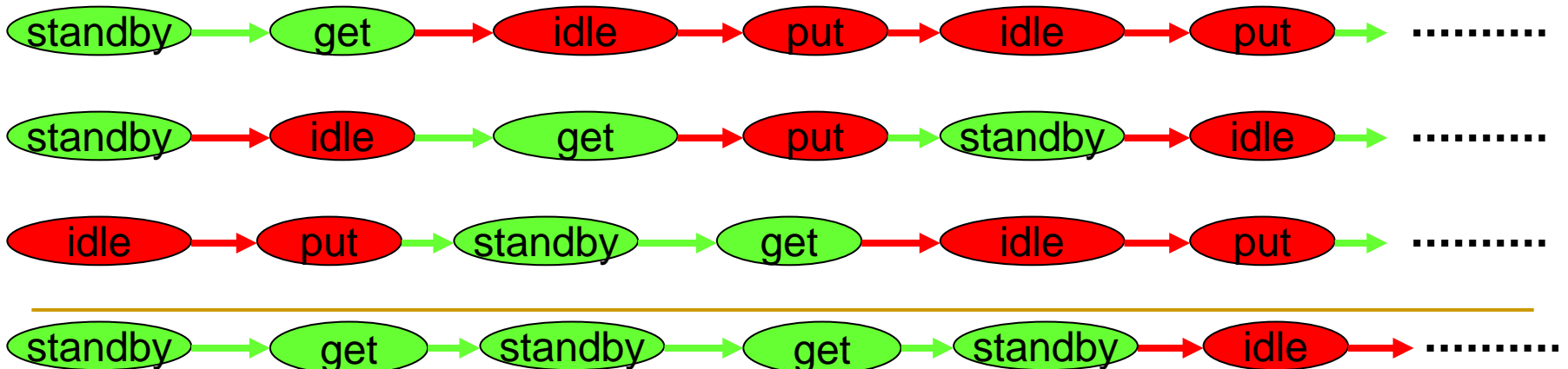
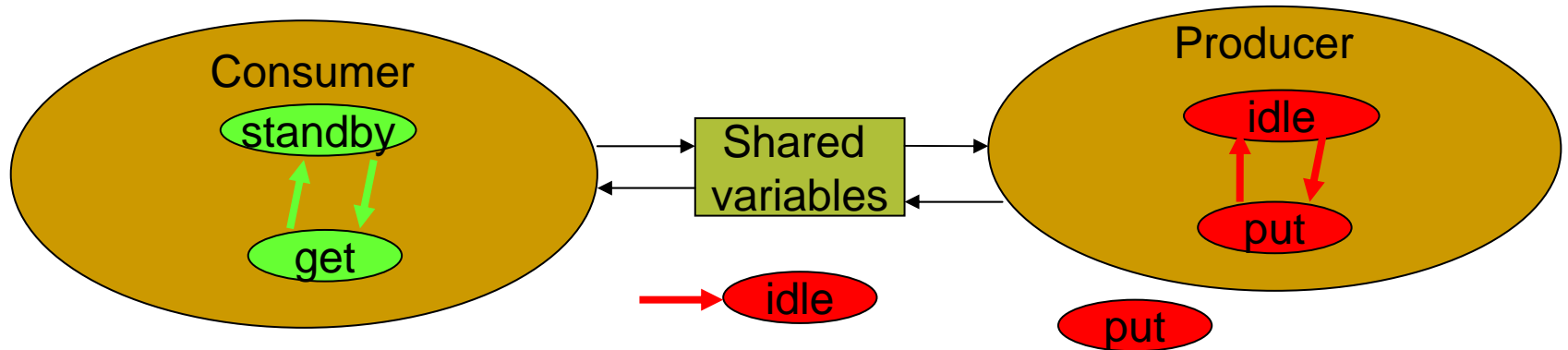
What is a legal concurrent computing ?



Communicating TA (CTA)

Interleaving Semantics

Atomic, instantaneous, non-overlapping

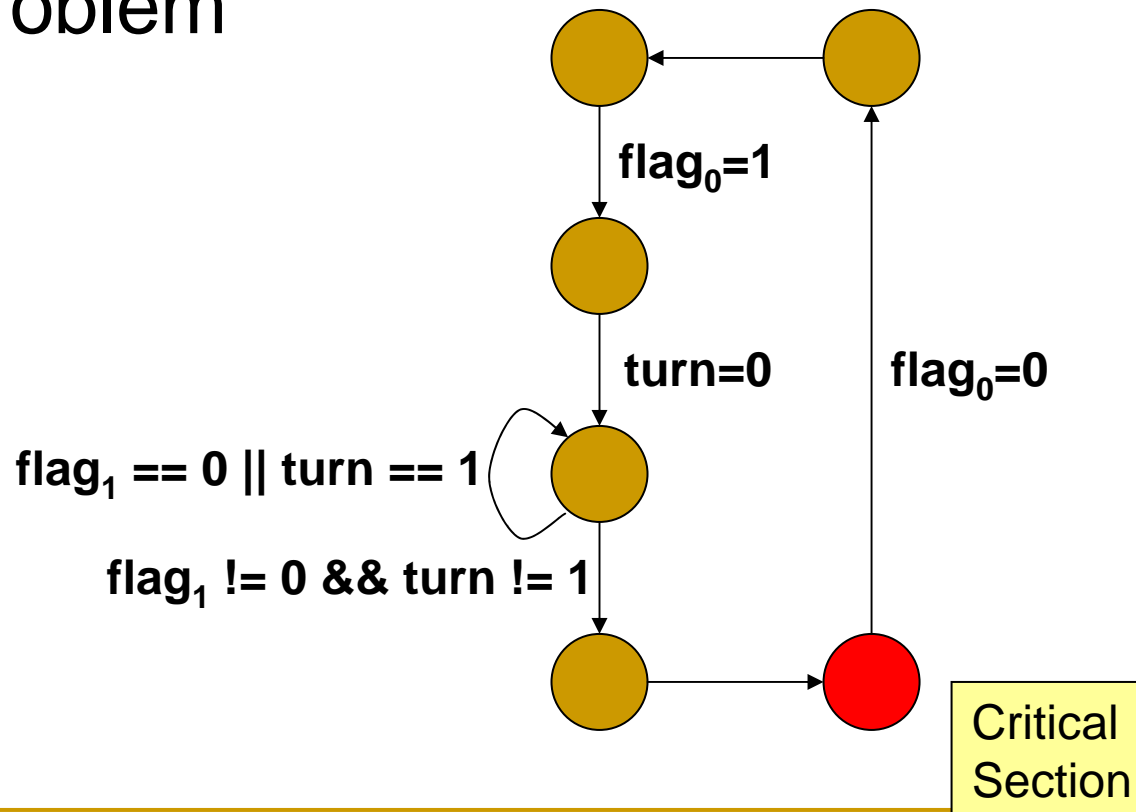


Model checking Examples

- A SPIN example for mutual exclusion problem
- A RED example for fisher's timed mutual exclusion algorithm

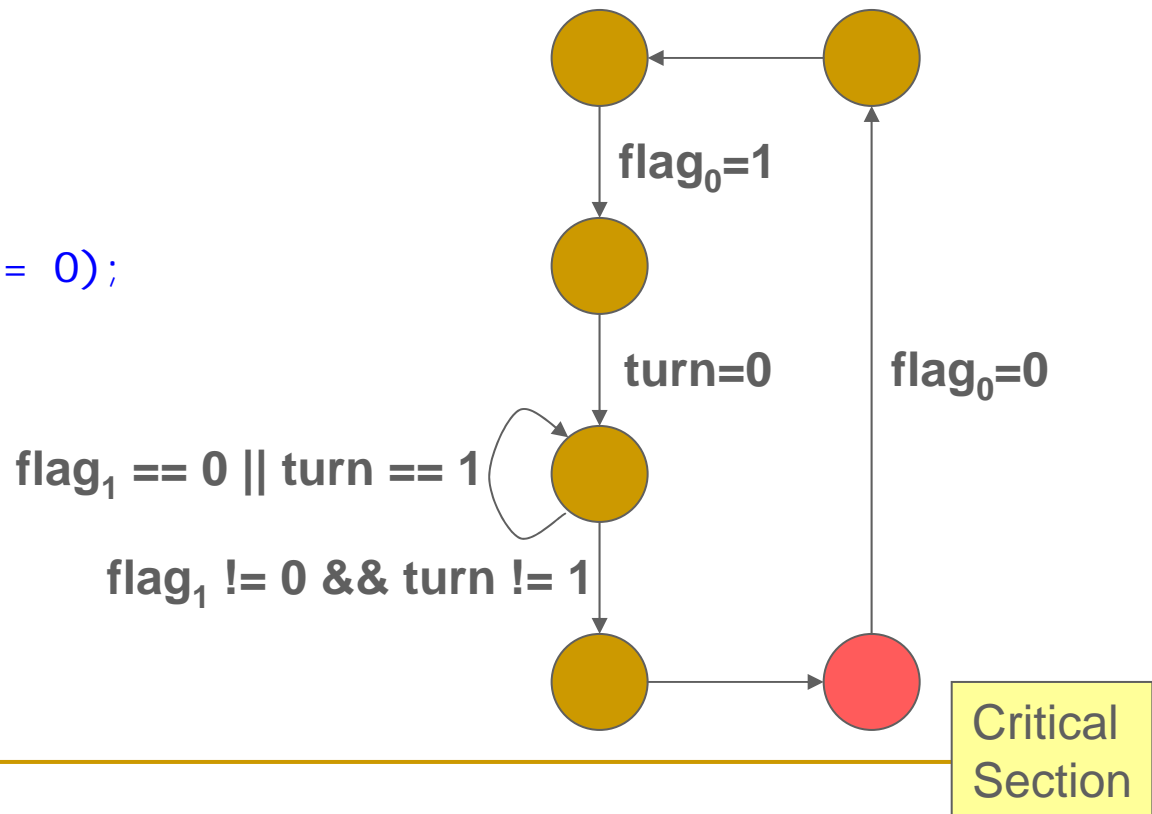
Mutual Exclusion

- Peterson's solution to the mutual exclusion problem

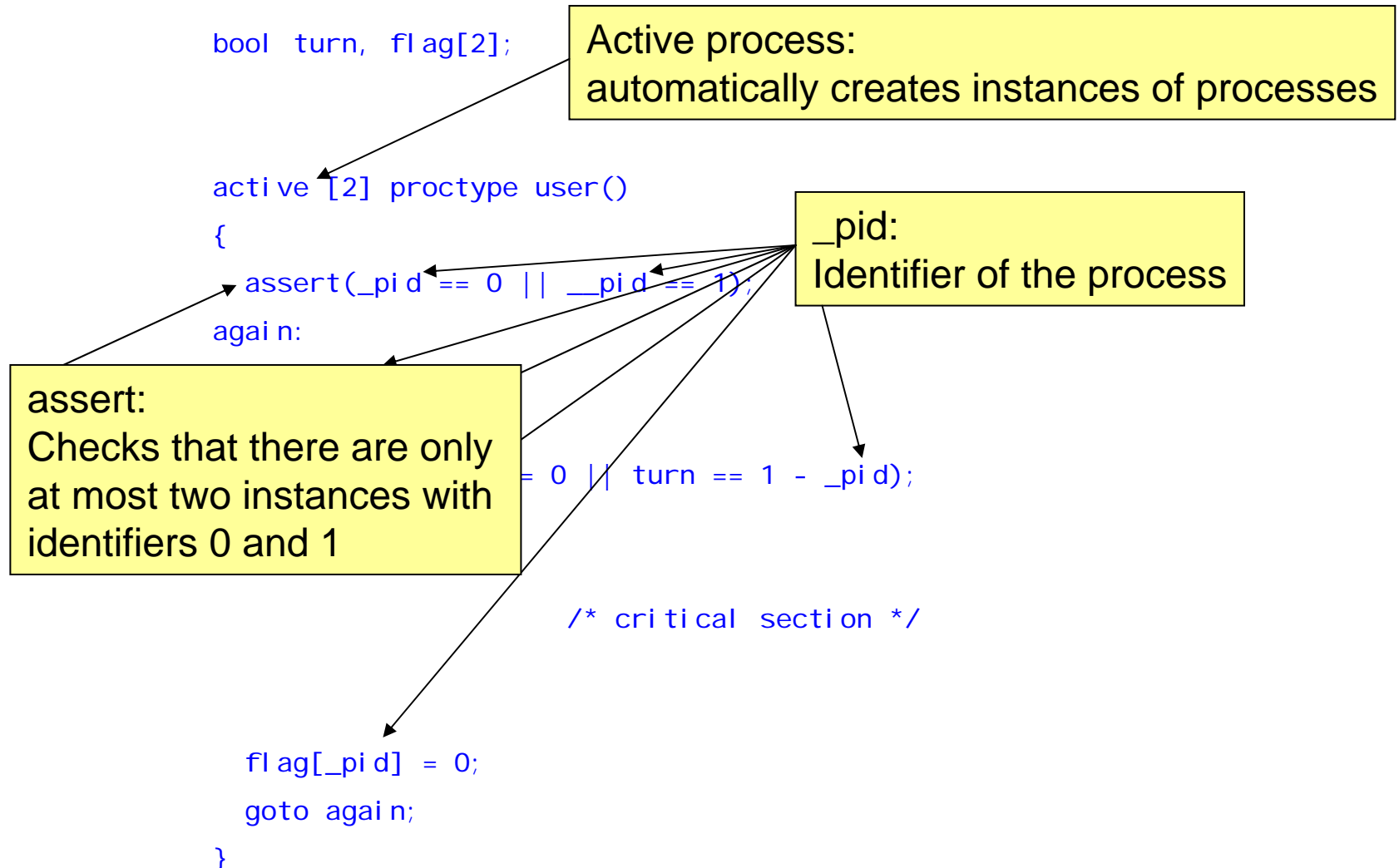


Mutual Exclusion in SPIN

```
bool  turn;
bool  flag[2];
proctype mutex0() {
again:
    flag[0] = 1;
    turn = 0;
    (flag[1] == 0 || turn == 0);
    /* critical section */
    flag[0] = 0;
    goto again;
}
```



Mutual Exclusion in SPIN



Mutual Exclusion in SPIN

```
bool turn, flag[2];
byte ncrit;

active [2] proctype user
{
    assert(_pid == 0 || _pid == 1);
again:
    flag[_pid] = 1;
    turn = _pid;
    (flag[1 - _pid] == 0 || turn == 1 - _pid);

    ncrit++;
    assert(ncrit == 1); /* critical section */
    ncrit--;

    flag[_pid] = 0;
    goto again;
}
```

ncrit:

Counts the number of
Process in the critical section

assert:

Checks that there are always
at most one process in the
critical section

Mutual Exclusion in SPIN

```
bool  turn, flag[2];
bool  critical[2];

active [2] proctype user()
{
    assert(_pid == 0 || _pid == 1);
again:
    flag[_pid] = 1;
    turn = _pid;
    (flag[1 - _pid] == 0 || turn == 1 - _pid);

    critical[_pid] = 1;
    /* critical section */
    critical[_pid] = 0;

    flag[_pid] = 0;
    goto again;
}
```

LTl Properties:

[] (critical[0] || critical[1])

[] <> (critical[0])

[] <> (critical[1])

[] (critical[0] ->
(critical[0] U
(!critical[0] &&
((!critical[0] &&
!critical[1]) U critical[1])))

[] (critical[1] ->
(critical[1] U
(!critical[1] &&
((!critical[1] &&
!critical[0]) U critical[0])))

CTA Examples

Fischer's timed mutual exclusion

A pointer variable *lock* initially NULL;

A clock variable *x* for each process;

Each process {

Initially at idle state;

idle: *lock* == NULL \rightarrow *x*=0; goto ready;

ready: if *x* <= A \rightarrow *x*=0; *lock* = P; goto waiting;

waiting: if *x* > B && *lock* == P \rightarrow goto critical;

if *lock* != P \rightarrow goto idle;

critical: *lock* = NULL; goto idle;

}

CTA: Fischer's timed mutual exclusion algorithm

Why mutual exclusion when $A < B$?

Consider a naive wrong locking algorithm.

```
While (true), do {  
    while (lock != NULL) ;  
    lock = P;  
}
```

Atomic operations: `lock != NULL` ----- (test)
 `lock = P;` ----- (assignment)

CTA: Fischer's timed mutual exclusion algorithm

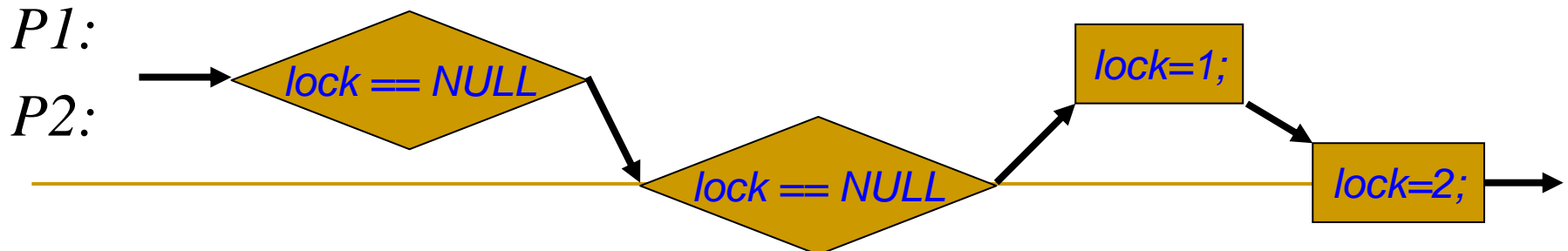
Why mutual exclusion when $A < B$?

How can this naïve algorithm be wrong?

```
While (true), do {  
    while (lock != NULL) ;  
    lock = P;  
    /* critical section */  
}
```

Interleaving can happen and mess up.

- distributed computing
- scheduling policy



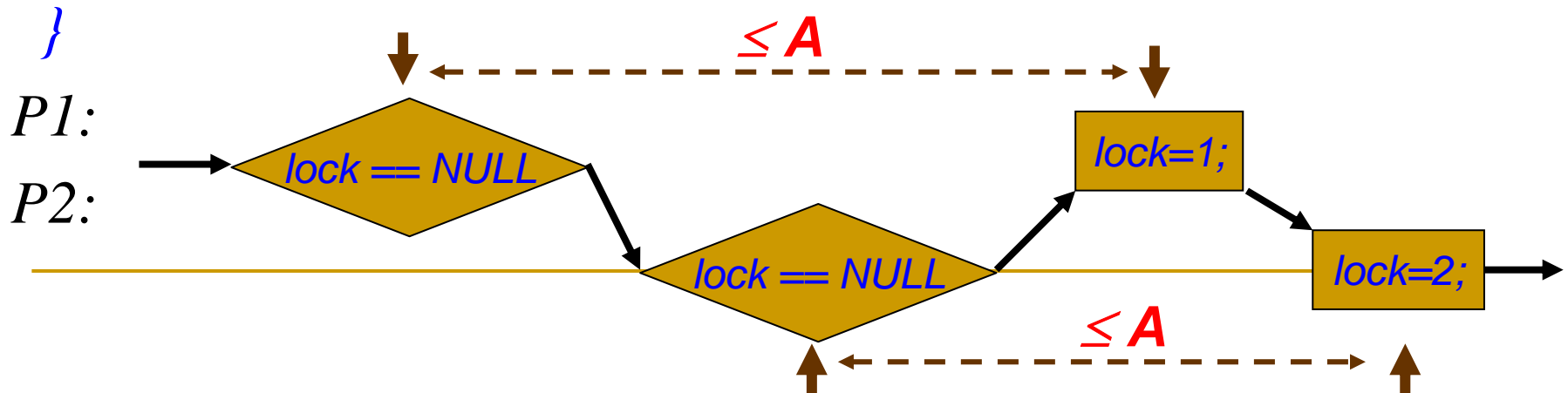
CTA: Fischer's timed mutual exclusion algorithm

Why mutual exclusion when $A < B$?

How can this naïve algorithm be wrong?

```
While (true), do {  
    while (lock != NULL) ;  
    lock = P;  
    /* critical section */  
}
```

But, assuming no scheduling mess-up,
how can $\text{lock} = P$ be postponed
indefinitely
in a concurrent system ?

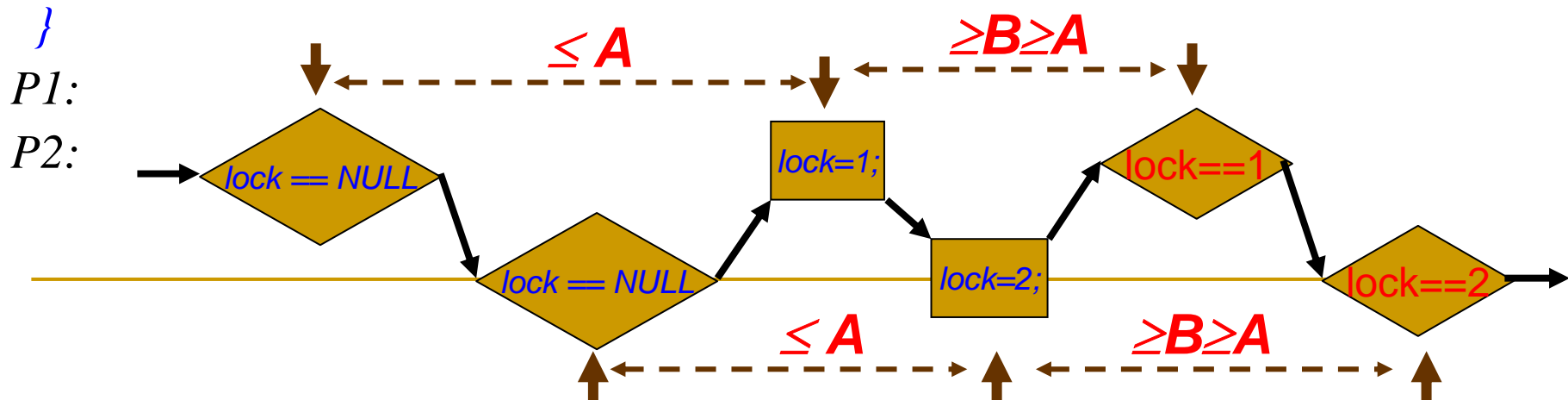


CTA: Fischer's timed mutual exclusion algorithm

Why mutual exclusion when $A < B$?

Remedy to the naïve algorithm:

```
While (true), do {  
    while (lock != NULL) ;  
    /* in between, take at most A sec. */  
    lock = P;  
    wait for  $B > A$  sec to enter critical section if lock = P still.  
    /* critical section */  
}
```

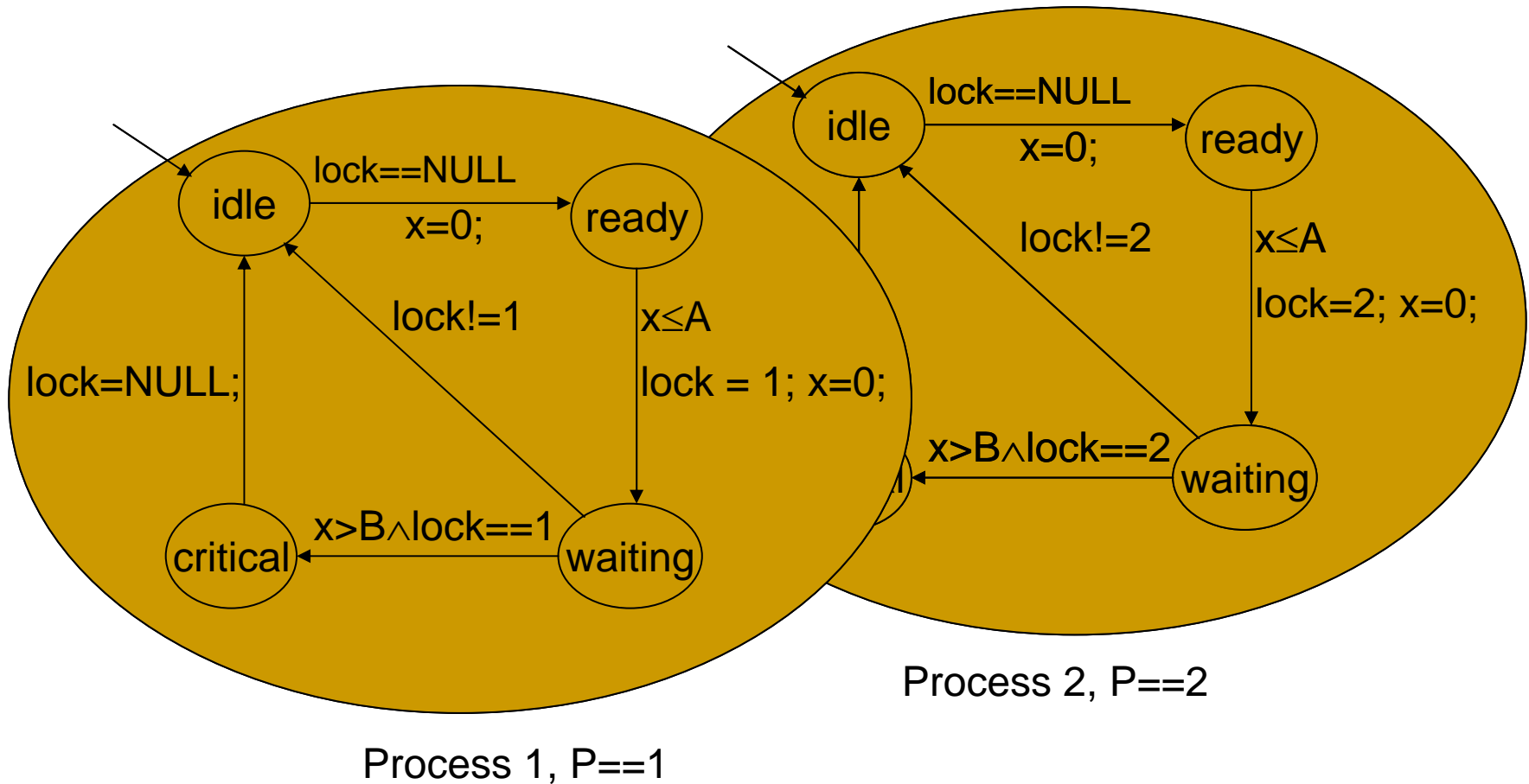


CTA: Fischer's timed mutual exclusion algorithm

Why mutual exclusion when $A < B$?

- *Assumption: all clocks advance their dense readings at the same rate.*
- 1. When a process is in **waiting**, no more process can enter **ready**.
- 2. A process test the lock in **waiting** only when all **ready** processes have entered **waiting**, since $A < B$.

CTA: Fischer's timed mutual exclusion algorithm



CTA: Fischer's algorithm in red format

```
/* Fischer's protocol with 2 processes */
```

```
process count = 2;
```

```
global pointer lock;
```

```
local clock x;
```

```
mode idle true {
```

```
  when lock == NULL may x= 0; goto ready;
```

```
}
```

```
mode ready true {
```

```
  when x < 10 may x= 0; lock= P; goto waiting;
```

```
}
```

```
mode waiting true {
```

```
  when (x > 19 and lock == P) may goto critical;
```

```
  when lock != P may goto idle;
```

```
}
```

```
mode critical true {
```

```
  when true may lock = NULL; goto idle;
```

```
}
```

```
initially lock == NULL and forall pi, (idle[pi] and x[pi] == 0);
```

```
risk critical[1] and critical[2];
```
