- #2: Z. Wu and T. Murata, "A Petri net model of a starvation-free solution to the dining philosopher's problem," IEEE Workshop on Languages for Automation, Chicago, November 7-9, 1983, pp. 192-195, 1983.
- #3: M. Lu, D. Zhang and T. Murata, "Analysis of self-stabilizing clock synchronization by means of Stochastic Petri Nets," *IEEE Transactions on Computers*, Vol.39, No.5, May 1990.
- #4: E. R. Boer and T. Murata, "Generating Basis Siphons and Traps of Petri Nets Using the Sign Incidence Matrix," *IEEE Transactions on Circuits and Systems*, pp.266 271, Vol. 41, No.4, April 1994
- #5: T. Smigelski, T. Murata and M. Sowa, "A timed Petri net model and simulation of dataflow computer," Procs. of the Int. Workshop on Timed Petri Nets, (Torino, Italy, July 1-3, 1985,) IEEE Computer Society Press, pp. 56-63, 1985.
- #6: G. Peterka and T. Murata, "Proof procedure and answer extraction in Petri net model of logic programs," IEEE Trans. on Software Eng., Vol.15, No.2, Feb., 1989, pp.209-217.
- #7: C. Lin and T. Murata, "A Petri Net Model for Inference in Non-Monotonic Logic," Institute of Electronics, Information and Communication Engineering ECE Trans. on Fundamentals of Electronics, Communications, and Computer Science., Vol. E77-A, No. 10, pp. 1577 1587, October 1994